

G. ADAM COVINGTON

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Summary

Computer Engineering graduate in search of a full time job. Special area interests include artificial intelligence, data mining, clustering, hardware-software design, and analysis. Self motivated dependable, with strong analytical, communication, and organizational skills. Willing to travel.

Education

Washington University in St. Louis

M.S. in Computer Engineering

Graduated: December 2006

Thesis: Architecture for Document Clustering in Reconfigurable Hardware

Courses:

Digital Logic, Technical Communication, Circuit Analysis, Microcontrollers, Network Analysis, Digital Design, Computer Architecture, Probabilistic Methods in Signal/Systems Analysis, Data and File Structures, Intro to Artificial Intelligence, Machine Learning, Data Security, SoC

Western Michigan University, Kalamazoo, Michigan

B.S. in Computer Engineering

Graduated: April 2003

University of Stuttgart; Summer University Program; Summer 2002

Technical Skill

Computer Languages: Assembly (Intel IXA, Motorola HC11/12), Visual Basic, C++, VHDL, Verilog
Operating Systems: Windows 95, 98, 2000, NT, XP, Linux, Unix
Software: Microsoft Office, Eagle CAD, Matlab, Mathcad, Pspice, Xilinx, Intel IXA SDK, Mentor Graphics; familiar with various Imaging/Graphic Programs

Conference Papers

Implementing an OpenFlow Switch on the NetFPGA Platform; Jad Naous, David Erickson, G. Adam Covington, Guido Appenzeller, Nick McKeown; ACM/IEEE Symposium on Architectures for Networking and Communications Systems; San Jose, CA; November 6-7, 2008.
Intelligent Avionics with Advanced Clustering; John Meier, Todd Sproull, G. Adam Covington, John W. Lockwood; IEEE Aerospace Conference; Big Sky, MT; March 3-8, 2008.
Streaming Hierarchical Clustering for Concept Mining; Moshe Looks, Andrew Levine, G. Adam Covington, Ronald P. Loui, John W. Lockwood, Young H. Cho; IEEE Aerospace Conference; Big Sky, MT; March 3-10, 2007.
High Speed Document Clustering in Reconfigurable Hardware; by G. Adam Covington, Charles L.G. Comstock, Andrew A. Levine, John W. Lockwood, Young H. Cho; 16th Annual Conference on Field Programmable Logic and Applications (FPL); Madrid, Spain; August 28-30, 2006.
HAIL: A Hardware-Accelerated Algorithm for Language Identification; by Charles M. Kastner, G. Adam Covington, Andrew A. Levine, John W. Lockwood; 15th Annual Conference on Field Programmable Logic and Applications (FPL); Tampere, Finland; August 24-26, 2005.
A 3-axis acceleration sensor data acquisition instrument system, by Asumadu, J.A.; La Belle, V.; Od'Neal, R.; Covington, G. A; Instrumentation and Measurement Technology Conference, 2004. IMTC 04. Proceedings of the 21st IEEE, Como, Italy May 18-20, 2004.

Design Projects

Decision Trees - Developed and analyzed pruned vs. non-pruned Decision Trees
Neural Networks - Designed Neural Network with a varying number of hidden layers and neurons
Bayesian Learning - Developed and analyzed a Naïve Bayesian Classifier
EM Algorithm - Searched for motifs without knowing the length of the motif
Network Attached Encryption Device - Created solution for encrypting credit card transactions
Soft Floating-Point Unit - Used the Intel IXP as a floating-point unit
Vision Programs - Used edge detection and smoothing filters for motion tracking
Guitar Pedals - Created circuit board layout, etched boards, soldered components, and tested performance

Honors & Activities

Distinguished Master of Science Fellowship/Scholarship at Washington University in St. Louis

Attended Bonior Congressional Camp sponsored by Senator Bonior

Certified SCUBA Diver

Baseball

Rock Climbing

Cycling